

PATENTS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Yoshiaki YAMAMOTO

Serial No. 09/089,666

Filed June 3, 1998



GROUP 2812

Examiner J. Hack

METHOD OF FABRICATING SEMI-
CONDUCTOR DEVICE FOR PREVENTING
RISING-UP OF SILISIDE

AMENDMENT

Assistant Commissioner for Patents

Washington, D.C. 20231

Sir:

Responsive to the Official Action of September 21, 1999, please amend the above-identified application as follows:

IN THE ABSTRACT:

Rewrite the Abstract of the Disclosure as on the accompanying separate sheet.

IN THE SPECIFICATION:

- Page 1, line 3, after "method" insert --of--;
- line 4, change "fablicating" to --fabricat-
ing--;
- line 5, after "method" insert --of--;
- line 8, change "LSIs" to --LSI--;
- line 9, cancel "a";
- line 11, cancel "a" second occurrence;
- line 17, after "by" insert --using--;
- line 20, change "increase" to --decrease--.

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